



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/852,882	05/10/2001	Mary Susan Huhn Eustis	03243-010001	5409

26161 7590 03/23/2005

FISH & RICHARDSON PC
225 FRANKLIN ST
BOSTON, MA 02110

EXAMINER

WINDER, PATRICE L

ART UNIT	PAPER NUMBER
----------	--------------

2145

DATE MAILED: 03/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/852,882

Applicant(s)

EUSTIS, MARY SUSAN HUHN

Examiner

Patrice Winder

Art Unit

2145

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 May 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>10-15-2002</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Wilkinson et al., USPN 5,966,528 (hereafter referred to as Wilkinson).

3. Regarding claim 1, Wilkinson taught a method for simultaneous processing of information (column 3, lines 23-29) comprising:

providing a matrix comprising a plurality of cells (column 6, lines 52-61), each cell of the plurality of cells comprising at least one processor and at least one page (column 24, lines 30-36), the at least one page comprising one selected from the set consisting of format counters, data pointers and processor counters (column 26, lines 40-46);

connecting at least one cell to at least one area of random access memory, the area comprising at least one addressable location (column 28, lines 59-64), and the connection being dynamically re-allocable to at least a second area of random access memory (column 28, lines 20-24; column 28, line 64 – column 29, line 7);

locating instructions and registers in random access memory areas (column 26, lines 59-66); and

processing information by at least one of the plurality of cells executing instructions pointed to by at least one process counter (column 24, lines 30-36, column 27, lines 58-62).

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Pincus et al., USPN 6,282,583 B1 (hereafter referred to as Pincus).

5. Regarding claim 1, Pincus taught a method for simultaneous processing of information (column 3, lines 45-55) comprising:

providing a matrix comprising a plurality of cells (column 9, lines 33-39), each cell of the plurality of cells comprising at least one processor and at least one page, the at least one page comprising one selected from the set consisting of format counters, data pointers and processor counters (column 10, line 59 – column 11, line 9);

connecting at least one cell to at least one area of random access memory, the area comprising at least one addressable location (column 12, lines 6-16, 35-40), and the connection being dynamically re-allocable to at least a second area of random access memory (column 13, lines 1-13);

locating instructions and registers in random access memory areas (column 15, lines 20-37); and

processing information by at least one of the plurality of cells executing instructions pointed to by at least one process counter (column 15, lines 48-56).

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

a. IBM TDB NN880741, High Performance/High Availability Interprocessor Communication Method: taught a multiprocessor configuration of N processors each requiring access to common system information. An identical copy of the common system information is stored in each of the N separate arrays. Each processor, together with its corresponding array, forms a cluster. High availability is provided because each cluster can fail and be repaired without impacting the operation of the subsystem.

b. IBM TDB NB9112190, MIMD Parallel Architecture with Critical Path Acceleration: taught an architecture for a parallel processor which speeds execution at the nodes by using a "Critical Path Hardware (CPH)" consisting of programmable hardware. A general MIMD parallel processor is modified to provide additional speed up. The CPH is used in a system that includes many node processors, each of which contains its own copy of the program and data, connected through a network.

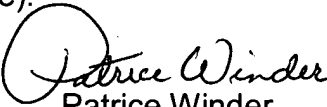
7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Patrice Winder whose telephone number is 571-272-3935. The examiner can normally be reached on Monday-Friday, 10:30 am-7:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Valencia Martin-Wallace can be reached on 571-272-6159. The fax phone

Art Unit: 2145

number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Patrice Winder
Primary Examiner
Art Unit 2145

March 18, 2005